



7 PIN SINGLE-IN-LINE PASSIVE DELAY LINE

FEATURES

- 7-PIN SIP PACKAGE.
- 5EQUALLY-SPACED TAPS.
- OPERATING TEMPERATURE RANGE:0°C TO +70°C
- LOW OUTPUT DISTORTION:±10%.
- TOTAL DELAYS FROM5-100Ns
(other values available upon request)

ELECTRICAL CHARACTERISTICS

Total Delay	:5 to 100ns
Tap Delays	:20% increments standard
Rise time	:SEE TABLE
Characteristic Impedance	:50 to 500 Ω(±10%)
Attenuation	:5%max
Distortion	:10% max
Temperature Coefficient of Delay	100ppm/°C max

INPUT PULSE TEST CONDITION

Pulse Voltage	:2.5V
Pulse Width	:5Td
Pulse Rise Time	:2nS(10% to 90%)

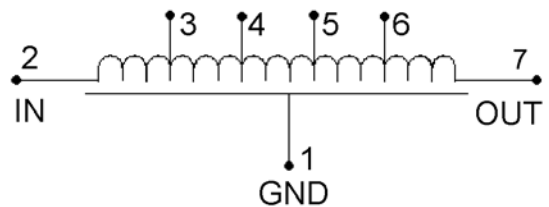
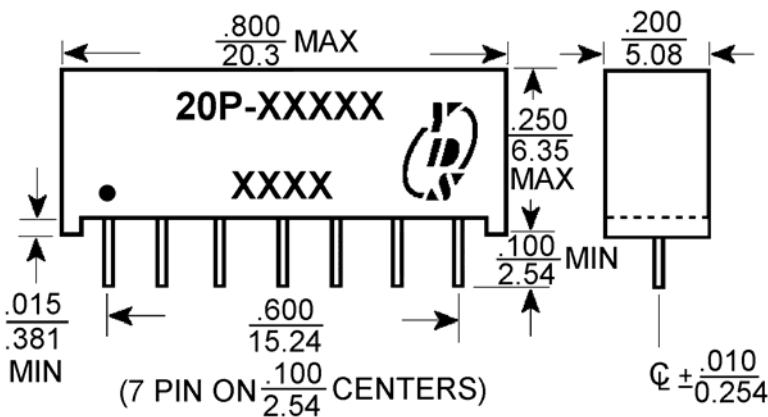


ELECTRICAL SPECIFICATIONS

PART NO.	TOTAL DELAY (ns)	TAP DELAY (ns)	RISETIME MAX(ns)	DC RESISTANCE(Ω)MAX PER IMPEDANCE(Ω)	
				50	100
20P-005XX	5±0.5	1±0.3	2.0	0.3	0.5
20P-010XX	10±1.0	2±0.4	3.3	0.5	1.0
20P-020XX	20±1.5	4±0.6	6.0	0.8	1.5
20P-030XX	30±2.0	6±1.0	9.0	1.2	2.5
20P-040XX	40±2.5	8±1.5	12.0	1.8	4.0
20P-050XX	50±3.0	10±1.8	15.0	2.3	5.0
20P-060XX	60±3.5	12±2.0	18.0	2.5	-
20P-070XX	70±4.0	14±2.0	22.0	3.5	-
20P-080XX	80±4.5	16±2.0	24.0	4.2	-
20P-090XX	90±5.0	18±3.0	27.0	4.5	-
20P-100XX	100±5.0	20±3.0	28.0	5.2	-

MARKINGS AND DIMENSION

PIN CONNECTIONS



Dimensions:inches/mm

Unless otherwise specified, all tolerances are ±.010/±0.25